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STATEMENT BY APPLICANT

First Named Inventor

Claude Chapel et al.

Art Unit

Examiner Name

(Use as many sheets as necessary)

Sheet 1 of 2 Attorney Docket Number PF020064

	U.S. PATENT DOCUMENTS							
Examiner	Cito	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
Initials *	Cite No. <sup>1</sup>	Number - Kind Code <sup>2</sup> (if known)	MM-DD-YYYY	Cited Document				
	AA	US-6359911	3/19/02	Movshovich et al.				
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	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>	
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	AB	EP 0971507	1/12/00	Thomson multimedia			
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	AC	R.H.J. Bloks: "THE IEEE-1394 HIGH SPEED SERIAL BUS", Philips Journal of Research, Elsevier, Amsterdam, NL, vol. 50, no. 1/2, 1 July 1996, pages 209-216			
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